

JAN 09 2004

OFFICIAL

Docket No.: 42390.P8517

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Kahn, et al

Serial No.: 09/228,817

Filed: 2/14/00

Title: (AS AMENDED) A METHOD AND
APPARATUS FOR RESUMING
MEMORY OPERATIONS UPON EXIT
FROM FROM A LOW LATENCY
WAKE-UP LOW POWER STATE)
)
)
)
) Examiner: Chang, Eric
)
) Art Unit: 2185
)
)
)#13/c
pay
3BOKAMENDMENT AND RESPONSE TO OFFICE ACTIONHon. Commissioner of
Patent & Trademarks
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Office Action mailed 9/9/03, applicants respectfully request entry
of the following amendment and consideration of the following remarks.

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Intel Corporation
2200 Mission College Blvd.
Santa Clara, CA 95052

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Intel Legal Team

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Urgent

Confidential

Date: 9-Jan-04

To:
Examiner Chang, Eric
USPTO

Fax:
703-746-7239

M/S:
Art Unit 2185

From:
Jeffrey S Draeger

Fax:
408-765-1621

M/S:
SC4-203

Subject:**Application Serial No. 09/228,817**

Title: A METHOD AND APPARATUS FOR RESUMING MEMORY OPERATIONS
UPON EXIT FROM FROM A LOW LATENCY WAKE-UP LOW POWER STATE

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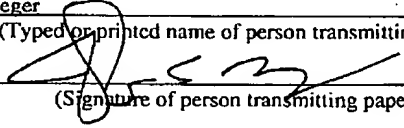
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